

10191/2197

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of : Alfred GOERLACH et al.
International Application No. : PCT/DE00/01812
International Filing Date : June 3, 2000
U.S. Serial No. : 10/030,309
For : **DIODE HAVING A METAL
SEMICONDUCTOR CONTACT,
AND METHOD FOR THE
MANUFACTURE THEREOF**

Assistant Commissioner for Patents
Box PCT
Washington, D.C. 20231

Attention: DO/EO/US

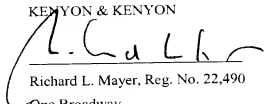
**RESPONSE TO MISSING REQUIREMENTS
UNDER 35 U.S.C. 371**

S I R :

In response to the Notification of Missing Requirements Under 35 U.S.C. 371 in the United States Designated/Elected Office (DO/EO/US) (mailed March 19, 2002), Applicants submit herewith a fully executed Declaration, in order to complete the filing requirements for the U.S. national phase of the above-identified PCT application. The application filed in the Patent Office is the application which the inventors executed by signing the Declaration and Power of Attorney. A copy of the Notification of Missing Requirements is enclosed.

The Office is authorized to charge the \$130.00 fee to cover the surcharge for late filing of the Declaration and any additional fees to Deposit Account No. 11-0600.

Respectfully submitted,
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